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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/118,010	07/17/1998	SHUNPEI YAMAZAKI	0756-1838	8550
	7590 07/02/2003		•	

ROBINSON INTELLECTUAL PROPERTY LAW OFFICE PMB 955 21010 SOUTHBANK STREET POTOMAC FALLS, VA 20165

EXAMINER

GUERRERO, MARIA F

ART UNIT PAPER NUMBER

2822 DATE MAILED: 07/02/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

		App	lication No.	Applicant(s)	
			18,010	YAMAZAKI E	ΤΛΙ
-	Office Action Summary	<i>.</i>	miner	Art Unit	
٤.	•		a Guerrero	2822	
	The MAILING DATE of this comn			,	e address
Period fo	r Reply			•	
THE I - Exter after - If the - If NO - Failu - Any r	ORTENED STATUTORY PERIOD MAILING DATE OF THIS COMMUNICATION OF THI	JNICATION. sions of 37 CFR 1.136(a). In communication. ty (30) days, a reply within t m statutory period will apply reply will, by statute, cause t ths after the mailing date of	no event, howeve he statutory minimu and will expire SIX he application to be	may a reply be timely filed im of thirty (30) days will be considered (6) MONTHS from the mailing date of toome ABANDONED (35 U.S.C. § 133)	his communication.
1)[Responsive to communication(s	s) filed on 29 April 20	003 .	·	
2a) <u></u>	This action is FINAL .	2b)⊠ This acti		l.	
3)□ Dispositi	Since this application is in condi closed in accordance with the pr on of Claims	ition for allowance e	xcept for forn	nal matters, prosecution as t	o the merits is
4)🛛	Claim(s) <u>1-8 and 11-73</u> is/are pe	ending in the applica	tion.		
•	4a) Of the above claim(s) i	s/are withdrawn from	m considerati	on.	
5)⊠	Claim(s) <u>1-8 and 11-46</u> is/are allo	owed.			
6)⊠	Claim(s) 47-73 is/are rejected.				
7)	Claim(s) is/are objected to) .			
8)□	Claim(s) are subject to res	striction and/or elect	ion requireme	ent.	
Applicati	on Papers				
9) 🗌 -	The specification is objected to by	the Examiner.			
10) 🔲 🗆	The drawing(s) filed on is/a	re: a) ☐ accepted or	b) objected	to by the Examiner.	
_	Applicant may not request that any				
11) 🔲 🗆	The proposed drawing correction f	filed on is: a)	approved	b)	miner.
	If approved, corrected drawings are			1.	
	The oath or declaration is objected	d to by the Examine	r.		
-	nder 35 U.S.C. §§ 119 and 120				
_	Acknowledgment is made of a cla		ty under 35 L	.S.C. § 119(a)-(d) or (f).	
a)[☑ All b) ☐ Some * c) ☐ None o	of:			
	1. Certified copies of the prior	rity documents have	been receive	ed.	
	2. Certified copies of the prior	rity documents have	been receive	ed in Application No. <u>08/962</u>	<u>.840</u> .
	 Copies of the certified copies application from the Interest attached detailed Office and the Interest and the Interest and I	ernational Bureau (PCT Rule 17.	been received in this Natio 2(a)). es not received.	nal Stage
	cknowledgment is made of a clair		•		onal application).
a	The translation of the foreign	language provision	al application	has been received.	
Attachment				gg	
2) Notice 3) Inform	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review		5) 🔲 No	erview Summary (PTO-413) Paper otice of Informal Patent Application her:	
I.S. Patent and Tr PTO-326 (Rev		Office Action Su	mmary	Part of Paper No.	. 51

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DETAILED ACTION

1. This Office Action is responsive to the Amendment filed March 24, 2003 and the Request for continued examination filed April 29, 2003.

Claims 9-10 are canceled.

Claims 1-8 and 11-73 are pending.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

- (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 2. Claims 47-73 are rejected under 35 U.S.C. 103(a) as being unpatentable over Wakai et al. (U.S. 5,055,899) in view of Takenouchi et al. (U.S. 5,427,961) and Mizushima et al. (U.S. 5,066,110) (of record).

Regarding claims 47-73, Wakai et al. discloses an inverted staggered TFT having a pixel electrode, an insulating substrate 101, a gate insulating film 103, and a semiconductor film 104 (amorphous silicon or the like) (col. 4, lines 15-30, col. 5, lines 40-45). Wakai et al. teaches an insulating film 108 can be comprising polyimide or an acrylic resin over a semiconductor layer 104 (col. 6, lines 2-10), a transparent electrode 110 made of ITO is a pixel electrode, and source and drain (106 and 107). Wakai et al. teaches the first insulating film 108a being used to flatten the uneven surface above the insulating substrate (fig. 7, col. 7, lines 48-57).

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Wakai et al. does not specifically show the second resinous substrate opposed to the first resinous substrate. However, Mizushima et al. discloses the semiconductor device having a pair of insulating substrates as conventional in the art (Abstract, Fig. 2, col. 5, lines 15-20, col. 7, lines 20-40).

Wakai et al. fails to disclose the substrate consisting of: polyethylene terephlate, polyethylene napthtalate, polyethylene sulfite and polyimide as claimed. Wakai fails to show the resinous material consisting of: methyl ester of acrylic acid, ethyl ester of acrylic acid, butyl ester of acrylic acid and 2-ethyhexyl ester of acrylic acid as claimed. However, this is known in the art as evidenced Takenouchi et al.

Takenouchi et al. discloses a semiconductor device having a resinous substrate, the resinous substrate made of polyester (e.g., PET (polyethylene terephlate)), polyimide, fluoroplastic, PES (polyethylene sulfane) (col. 3, lines 49-55). Takenouchi et al. also teaches a resinous layer provided on the resinous substrate including an acrylic resin (e.g. methyl acrylate ester, ethyl acrylate ester, butyl acrylate ester, and 2-ethyhexyl acrylate ester (col. 3, lines 55-60). In addition, Takenouchi et al. discloses providing the film on the substrate with the purpose of leveling the initial surface irregularities (col. 4, lines 10-15).

It would have been obvious to a person of ordinary skill in the art at the time the invention was made to modify Wakai et al's semiconductor device by including the teaching of Takenouchi et al. and Mizushima et al. The modification would provide a low cost semiconductor device easily handled having a larger field of application and free from oligomeros (Takenouchi et al., col. 1, lines 15-25, col. 3, lines 20-25).

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Allowable Subject Matter

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3. Claims 1-8 and 11-46 are allowed.

4. The following is a statement of reasons for the indication of allowable subject matter: The Terminal Disclaimer filed September 28, 2001 overcome the Double Patenting Rejection.

Response to Arguments

5. Applicant's arguments with respect to claims 47-73 have been considered but are moot in view of the new ground(s) of rejection.

Conclusion

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Maria Guerrero whose telephone number is (703) 305-0162.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Amir Zarabian, can be reached on (703) 308-4905. The fax phone number for this Group is (703) 308-7722.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is (703) 308-0956.

Maria Guerrero

Patent Examiner

June 30, 2003